

To: U.S. Department of Commerce
(PW FORM PAT-1449)
Patent and Trademark Office

Dkt. No.

0249729

P12821

Applicant: Gangadhar D. Bhat

Appln. No.: Not Assigned

Filing Date: January 30, 2002

Examiner: Unknown

Group Art Unit: Unknown

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Date: January 30, 2002

Page

1

of

1

U.S. PATENT DOCUMENTS

| Examiner's Initials* | Document Number | Date MM/YYYY | Name (Family Name of First Inventor) | Class | Sub Class | Filing Date (if appropriate) |
|-------------------------|--------------------|-----------------|---|-------|--------------|------------------------------------|
| | AR | | | | | |
| | BR | | | | | |
| | CR | | | | | |
| | DR | | | | | |
| | ER | | | | | |
| | FR | | | | | |
| | GR | | | | | |
| | HR | | | | | |
| | IR | | | | | |
| | JR | | | | | |
| | KR | | | | | |
| | LR | | | | | |
| | MR | | | | | |
| | NR | | | | | |

FOREIGN PATENT DOCUMENTS

| | Document Number | Date MM/YYYY | Country | Inventor Name | | English Abstract | | Translation Readily Available | |
|--|--------------------|-----------------|---------|---------------|--|---------------------|----|-------------------------------------|----|
| | | | | | | Enclosed | No | Enclose | No |
| | OR | | | | | | | | |
| | PR | | | | | | | | |
| | QR | | | | | | | | |
| | RR | | | | | | | | |
| | SR | | | | | | | | |
| | TR | | | | | | | | |
| | UR | | | | | | | | |
| | VR | | | | | | | | |
| | WR | | | | | | | | |
| | XR | | | | | | | | |

OTHER (including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

| | | | | |
|-----|---|--|--|--|
| YR | http://developer.intel.com/design/network/products/npfamily/ixp1200.htm "Intel IXP1200 Network Processor Family." pp 1 - 3. | | | |
| ZR | | | | |
| AAR | | | | |
| BBR | | | | |
| CCR | | | | |
| DDR | | | | |

Examiner

Date Considered: 3/02/05